INDUSTRIAL A.I. & MACHINE LEARNING
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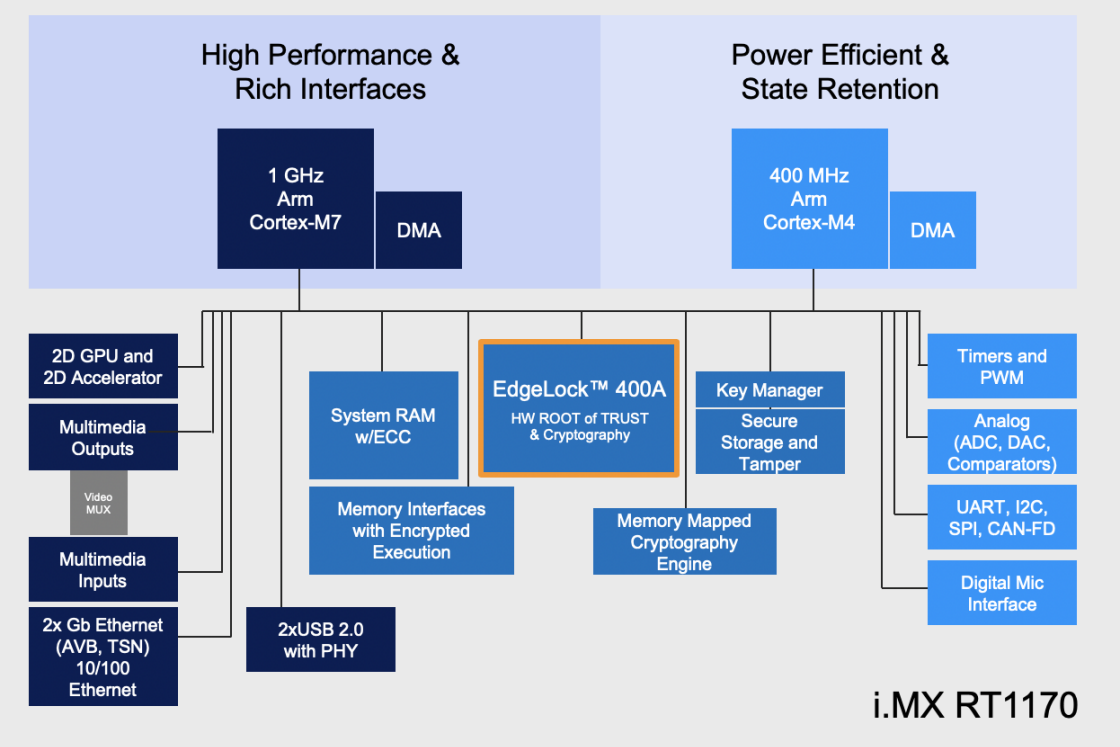
Thursday 10/3

AI PROCESSING FEATURE

NXP Releases Dual-Core GHz Crossover MCU for Edge ML

NXP has released the i.MX RT1170 crossover family, the first MCUs in the industry capable of running at up to 1 GHz. The devices are based on a dual-core Arm Cortex-M architecture, featuring an -M7 core able to run at 1 GHz and -M4 core that runs at 400 MHz. These are accompanied by a 2D pixel-processing vector graphics engine and NXP EdgeLock security.

Read more



NEURAL NETWORK NEWS

Aldec to Exhibit Deep Neural Network and Machine Learning at Arm TechCon

Aldec, Inc. will be showcasing the development of Deep Neural Network (DNN) and Machine Learning (ML) applications at Arm TechCon from October 8-10 in San Jose, CA.

Read more

MACHINE VISION NEWS

MVTec Presents HALCON 19.11 Updated Machine-Vision Software

MVTec Software will release HALCON 19.11 on November 15, which will include many new and improved functions for machine vision, such as an anomaly detection function to allow deep-learning-based inspection tasks to be implemented more efficiently.

Read more

AI AUTOMOTIVE NEWS

Infineon and Synopsys Join Forces on Implementing AI into Automotive Applications

Infineon Technologies? AURIX microcontrollers back AI-driven solutions that implement a new high-performance AI accelerator called a Parallel Processing Unit (PPU) that will leverage Synopsys? DesignWare ARC EV Processor IP.

Read more

AI MEMORY NEWS

Micron Xccella Flash to Drive Xilinx Versal ACAP AI Acceleration Platforms

Xilinx has adopted Micron?s Xcella flash memory for its Versal adaptive compute acceleration platform (ACAP). Xccella?s JEDEC xSPI-compliant bus interface delivers an 8x boot and configuration performance improvement over quad SPI NOR flash in Xilinx Versal ACAP platforms.

Read more

AI PROCESSING NEWS

MIPI CSI-2 v3.0 Enhances Imaging, Reduces Cabling for Embedded Systems Designers

The MIPI Alliance has updated its Camera Serial Interface-2 (CSI-2) specification with Unified Serial Link (USL), Smart Region of Interest (SROI), and RAW-24 features. Version 3.0 of the specification is also accompanied by new tools and companion specifications for developer enablement, including the MIPI Camera Command Set (MIPI CCS) and a Conformance Test Suite (CTS).

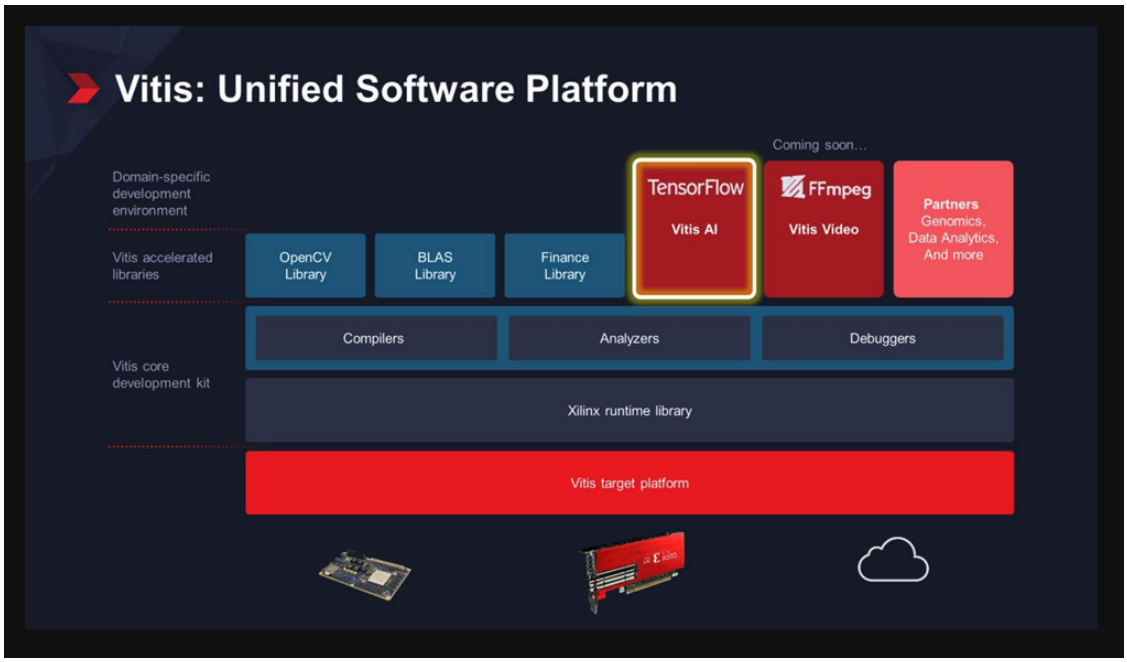
Read more

AI HARDWARE NEWS

Xilinx Vitis Platform Delivers Benefits of Adaptable Hardware to Software Developers, AI Engineers

Xilinx has launched Vitis, a free and open-source development platform that automatically adapts software and algorithms to Xilinx hardware without the need for VHDL or Verilog expertise.

Read more



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