AI Can Keep Industries on Track, Literally
GERARD CRONIN, STMICROELECTRONICS

Artificial Intelligence (AI) is finding application across a broad swath of industrial applications. One of these is industrial sensor systems for trains. Lenord + Bauer, a company typically known for developing products for measuring technology in motion automation, created an intelligent sensor based on an accelerometer and STM32 microcontroller on an evaluation board (the STEVAL-BFA001V1B).

Cellular V2X vs. ITS-G5: Which Will Dominate Autonomous Car Connectivity?
MAJEED AHMAD, EMBEDDED COMPUTING DESIGN

The autonomous car movement is about to face another roadblock: a battle over the connectivity standard between the 5G-based cellular vehicle-to-everything (V2X) communications and the ITS-G5 technology based on Wi-Fi connectivity.

Qt World Summit hits Berlin: UX Designers Take Note
RICH NASS & RORY DEAR, EMBEDDED COMPUTING DESIGN
The Qt Company’s annual World Summit hit Berlin last week, a city that’s become increasingly popular for start-up tech companies. The messages delivered were clear: no longer can User Experience (UX) be an afterthought; it’s analogous to the criticality of integrating security and must be considered and implemented at every stage of the design process.

Read More +

PODCAST

Embedded Insiders Tackle RISC-V Summit: Get Our Take on WD, NXP Announcements
EMBEDDED COMPUTING DESIGN

From sheer numbers alone, the open-source instruction-set architecture (ISA) known as RISC-V is gaining momentum in a pretty big way. And the announcements from the event were aplenty. Two big ones, from Western Digital (WD) and NXP (sort of, through the Open-ISA.org) prove that it’s all about the ecosystem, and both vendors are doing their part to make that ecosystem grow into something sustainable.

Read More +

FEATURE

Article Image
New Debugging Initiatives for Today’s New IoT World
CURT SCHWADERER, EMBEDDED COMPUTING DESIGN

OzCode is a company aiming to change the paradigm and make debugging more efficient and have announced an innovative Debugging-as-a-Service environment aimed at making development faster, more efficient, and more correct.

Read More +

FEATURE

Article Image
Bringing OPC UA and Ethernet to field level with Time-Sensitive Networking
EMBEDDED COMPUTING DESIGN

The OPC Foundation recently launched a new taskforce with the goal of integrating OPC UA and TSN to join the
Looking to Reduce the Cost and Complexity of Power Measurement?

Microchip’s single, two-channel PAC1932 improves accuracy and reduces power consumption by measuring power from 0V to 32V on different rails.

With native 16-bit resolution the PAC1932, and the three-channel PAC1933, replace separate components and enhance measurement precision in low-voltage, high-power applications. Design is simplified without the need to reconfigure measurement resolution between low- and high-current load events.

European Processor Initiative Designates Menta Provider of eFPGA IP

Menta eFPGA fabrics, memory, and Origami Designer specification software help SoC architects meet high-performance compute (HPC) and automotive electronic control unit (ECU) silicon requirements.

Imperas, Valtrix Partner for RISC-V Processor Verification

Imperas Software and Valtrix Systems are collaborating on advanced RISC-V Processor test and validation solutions using Valtrix STING SoC design verification tool and Imperas riscvOVPsim RISC-V instruction set simulation (ISS).

SEGGER Releases New emPower-USB-Host

SEGGER’s emPower-USB-Host is a microcontroller with two USB host ports, creating easy access to different types of USB devices.
Case Study: Embedded Computing Design at Embedded World
We believe in big numbers
OPENSYSTEMS MEDIA
OpenSystems Media believes in BIG numbers. Over the past 30 years, OpenSystems Media has consistently led the industry in lead generation, especially when it comes to events like Embedded World.

Read More +

Get started with industrial Ethernet on Sitara? AM65x processor
Sponsored by: Texas Instruments
Date: January 23, 11:00 a.m. ET
REGISTER NOW