

DECEMBER 2018

FEATURE



[Article Image](#)

AI Can Keep Industries on Track, Literally

GERARD CRONIN, STMICROELECTRONICS

Artificial Intelligence (AI) is finding application across a broad swath of industrial applications. One of these is industrial sensor systems for trains. Lenord + Bauer, a company typically known for developing products for measuring technology in motion automation, created an intelligent sensor based on an accelerometer and STM32 microcontroller on an evaluation board (the STEVAL-BFA001V1B).

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FEATURE



Cellular V2X vs. ITS-G5: Which Will Dominate Autonomous Car Connectivity?

MAJEED AHMAD, EMBEDDED COMPUTING DESIGN

The autonomous car movement is about to face another roadblock: a battle over the connectivity standard between the 5G-based cellular vehicle-to-everything (V2X) communications and the ITS-G5 technology based on Wi-Fi connectivity.

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FEATURE

Qt World Summit hits Berlin: UX Designers Take Note

RICH NASS & RORY DEAR, EMBEDDED COMPUTING DESIGN



The Qt Company's annual World Summit hit Berlin last week, a city that's become increasingly popular for start-up tech companies. The messages delivered were clear: no longer can User Experience (UX) be an afterthought; it's analogous to the criticality of integrating security and must be considered and implemented at every stage of the design process.

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PODCAST



Embedded Insiders Tackle RISC-V Summit: Get Our Take on WD, NXP Announcements

EMBEDDED COMPUTING DESIGN

From sheer numbers alone, the open-source instruction-set architecture (ISA) known as RISC-V is gaining momentum in a pretty big way. And the announcements from the event were aplenty. Two big ones, from Western Digital (WD) and NXP (sort of, through the Open-ISA.org) prove that it's all about the ecosystem, and both vendors are doing their part to make that ecosystem grow into something sustainable.

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FEATURE



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New Debugging Initiatives for Today's New IoT World

CURT SCHWADERER, EMBEDDED COMPUTING DESIGN

OzCode is a company aiming to change the paradigm and make debugging more efficient and have announced an innovative Debugging-as-a-Service environment aimed at making development faster, more efficient, and more correct.

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FEATURE



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Bringing OPC UA and Ethernet to field level with Time-Sensitive Networking

EMBEDDED COMPUTING DESIGN

The OPC Foundation recently launched a new taskforce with the goal of integrating OPC UA and TSN to join the

industrial enterprise with operational devices in the field.

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Looking to Reduce the Cost and Complexity of Power Measurement?

Microchip's single, two-channel PAC1932 improves accuracy and reduces power consumption by measuring power from 0V to 32V on different rails.

With native 16-bit resolution the PAC1932, and the three-channel PAC1933, replace separate components and enhance measurement precision in low-voltage, high-power applications. Design is simplified without the need to reconfigure measurement resolution between low- and high-current load events.

NEWS



European Processor Initiative

European Processor Initiative Designates Menta Provider of eFPGA IP

EMBEDDED COMPUTING DESIGN

Menta eFPGA fabrics, memory, and Origami Designer specification software help SoC architects meet high-performance compute (HPC) and automotive electronic control unit (ECU) silicon requirements.

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NEWS

Imperas, Valtrix Partner for RISC-V Processor Verification

EMBEDDED COMPUTING DESIGN

Imperas Software and Valtrix Systems are collaborating on advanced RISC-V Processor test and validation solutions using Valtrix STING SoC design verification tool and Imperas riscvOVPsim RISC-V instruction set simulation (ISS).

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NEWS

SEGGER Releases New emPower-USB-Host

EMBEDDED COMPUTING DESIGN

SEGGER's emPower-USB-Host is a microcontroller with two USB host ports, creating easy access to different types of USB devices.

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NEWS

STMicro's Energy-Saving Low-Noise LDO Regulator Runs Automotive Modules and Smart Automation

EMBEDDED COMPUTING DESIGN

The LDO40L is AEC-Q100 qualified with 45µA quiescent current and diminishes the need for vehicle batteries to run loads while the ignition is off. The device can also be deactivated to achieve a very-low shutdown current of only 1µA.

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NEWS

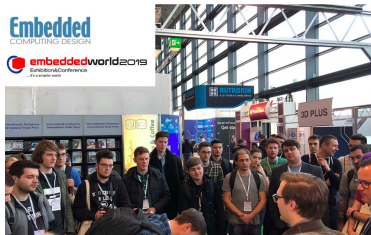
Renishaw uses Proven LDRA Tool Suite for Functionally Safe Positioning

EMBEDDED COMPUTING DESIGN

Renishaw has certified its RESOLUTE FS optical encoder system to meet level SIL 2 in accordance with IEC 61508-1:2010 by successfully implementing the LDRA tool suite.

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