DEVELOPMENT TOOLS & OPERATING SYSTEMS FEATURE

It’s an Exciting Time for Software Developers Who Want to Use FPGAs

MAX MAXFIELD, FREELANCE TECHNOLOGY WRITER

The vast majority of embedded software developers focus on coding for MPUs and MCUs; relatively few have any experience with regard to using field-programmable gate arrays (FPGAs). This is unfortunate because although regular processors are great for performing decision-making and control chores, they are horribly inefficient when it comes to performing many data processing tasks.

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IoT SECURITY FEATURE

Another IoT Security "Uh-Oh": 26 Flaws in Open-Source Zephyr and MCUboot Stacks

BRANDON LEWIS, EDITOR-IN-CHIEF- EMBEDDED COMPUTING DESIGN

NCC Group, one of the world's largest security consulting firms, recently conducted an independent research examination that analyzed the security posture of both the Zephyr RTOS and MCUboot secure bootloader, and found more 26 vulnerabilities of varying severity.

Jennifer Fernick, Head of Research at NCC, spoke with Embedded Computing Design about findings from the
AI & MACHINE LEARNING FEATURE

AI Technologies to Battle against COVID-19
SAM LIAO, CHIEF TECHNOLOGY OFFICER, WINMATE INC

AI technologies, which were called "futuristic" not so long ago, are proving to be an effective tool contributing to fight the pandemic in healthcare and public health applications.

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AI & MACHINE LEARNING FEATURE

Machine Vision in the mW Range Makes IoT Endpoint Inferencing Practical
SEMIR HADDAD, SENIOR DIRECTOR OF PRODUCT MARKETING, ETA COMPUTE

IoT endpoints lie at the frontier of embedded vision. And, as with other frontiers, there are challenges, not least of which is power efficiency. Can inferencing at the extreme edge happen without exceeding the node’s power capacity?

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DEVELOPMENT TOOLS & OPERATING SYSTEMS NEWS

QuickLogic Announces Open Reconfigurable Computing Initiative
TIERA OLIVER, EDITORIAL INTERN, EMBEDDED COMPUTING DESIGN

QuickLogic Corporation announced its QuickLogic Open Reconfigurable Computing (QORC) initiative. According to the company, this makes QuickLogic the first programmable logic vendor to actively embrace a fully open source suite of development tools for its FPGA devices and eFPGA technology.
IoT PROCESSING FEATURE

Edge Analytics Complementing Cloud Computing

KAUMIL DESAI, VOLANSYS

Due to lack of reliable connectivity, delays and difficulties in processing this huge data on cloud, there is a challenge in analyzing and extracting important insights from this data. To deal with this challenge, enterprises are leveraging edge analytics along with cloud computing.

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DEVELOPMENT TOOLS & OPERATING SYSTEMS FEATURE

Bottom Up or Top Down?

COLIN WALLS, MENTOR

There are, broadly speaking, two distinct approaches to the design of software: bottom-up or top-down. It is most common to approach it top-down. Design, say, the user interface first and the underlying functionality afterwards. The implementation of the design ? the actual coding of the software ? is another matter.

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projects appear to be progressing nicely. The organization began with just a handful of vendors and has now grown to more than 500. To that end, Dave Kjendal, CTO and Executive Vice President of Engineering at Senet, has just joined the Alliance’s Board of Directors. It seemed like a good time to speak to Dave to hear what his ideas and direction are for the Alliance. Dave is my guest on this week’s Embedded Executives podcast.

Tune In +

**IoT PROCESSING FEATURE**

**Discover What’s Waiting for You at the Edge**

DAN DEMERS, DIRECTOR, SALES & MARKETING, CONGATEC AMERICAS

Look around. It’s clear that more and more users are relying on edge servers for virtualization of their Industry 4.0 applications, such as machine manufacturers, systems manufacturers and industrial end users. These edge servers excel at consolidating the distribution of a spectrum of applications and controls across a manufacturing cell, for example.

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**WHITE PAPER**

**Knowing the Difference Between Code Size and Footprint Could Shrink Your Memory Needs**

MCOBJECT
The terms "code size" and "footprint" are often used interchangeably. But, they are not the same; code size is a subset of footprint. This paper will explain the differentiation and relevance, then proceed to describe some techniques to minimize footprint.

**WHITE PAPER**

**High Performance Edge Computing - The Frontier for Supercomputing and Acceleration**

EUROTECH

High Performance Edge Computing (HPEC) means bringing high-performance computing capabilities - typically found in data center infrastructures - into hardware systems deployable at the edge, where environmental conditions are typically harsh.

**WHITE PAPER**

**Air quality measurement gets personal with advanced PM2.5 and VOC sensor technologies**

BOSCH

It is all around us, but unless there is a problem, we usually do not concern ourselves with what is in the air that we breathe. In both indoor and outdoor environments, poor air quality can greatly impact our health and well-being.

**Inpatient Telehealth: Lessons from the Pandemic**

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