MEMORY & STORAGE FEATURE

Wear Levelling and How it Impacts SSD Life Expectancy

Data in flash memory is arranged in a hierarchy of cells, pages, and blocks, with data written a page at a time. NAND flash memory architecture is such that erasing can only occur at the block level. Also, access to the data in a solid state drive (SSD) is usually localized. There will be some data that is rarely used, or only accessed for reading, and other data that is frequently updated.

However, there is a limit to the number of times flash cells can be erased.
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EMBEDDED SENSORS NEWS

D3 Engineering Announces Production-Intent Antenna-on-Package mmWave Radar Sensors

D3 Engineering announced their DesignCore RS-6843AOP and RS-6843AOPU mmWave Radar Sensors. Per the company, these miniature sensors enable the implementation of many different mmWave radar algorithms to measure, detect, and track.
Read more

AI AND MACHINE LEARNING NEWS

NVIDIA Extends Lead on MLPerf Benchmark with A100

NVIDIA announced its AI computing platform has again achieved ideal performance records in the latest round of MLPerf. According to the company, the results led to NVIDIA extending its lead on the industry?s only independent benchmark measuring AI performance of hardware, software, and services.

Read more

INDUSTRY 4.0 NEWS

Neurala Announces Partnership with IMA Group to Accelerate AI Technology for Industry 4.0 Initiatives

The partnership is designed to provide AI solutions for industrial machines, focused on field testing of AI that enables monitoring and provides actionable insights from data collected through industrial internet of things (IIoT) systems.

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DEVELOPMENT TOOLS & OPERATING SYSTEMS FEATURE

A Simple Way to Measure Temperature Using One GPIO Digital Interface

Question: How can I make an analog measurement if I only have a single GPIO left on the FPGA/microprocesser for my system?

Answer: A voltage-to-frequency converter can be used instead of an analog-to-digital converter.

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