

AI COMPUTING BLOG

NXP Debuts i.MX Applications Processor with Neural Processing Unit for Advanced Machine Learning

The i.MX 8M Plus combines an NPU delivering 2.3 TOPS (Tera Operations Per Second) with a Quad-core Arm Cortex-A53 sub-system running at up to 2GHz, an independent real-time sub-system with an 800MHz Cortex-M7, a 800 MHz audio DSP for voice and natural language processing, dual camera Image Signal Processors (ISP), and a 3D GPU.

[Read more](#)



COMPUTER VISION NEWS

Infinion Claims Smallest 3D Image Sensor for Face Authentication

The depth sensor time-of-flight technology enables an accurate 3D image of faces, hand details, or objects. The chip also provides additional options for photos, with enhanced autofocus, bokeh effect for photo and video, and improved resolution in poor lighting conditions.

[Read more](#)

INTELLIGENT SENSING NEWS

CEVA Debuts SenslinQ Platform to Streamline Development of Contextually Aware IoT Devices

After applying advanced algorithms, SenslinQ creates context enablers such as activity classification, voice and sound detection, and presence and

proximity detection. These context enablers can then be fused on-device or otherwise sent wirelessly (Bluetooth, Wi-Fi, NB-IoT) to a local edge computer or the cloud.

[Read more](#)

COMPUTER VISION NEWS

ams Offers Evaluation Kit for Their NanEyeC Miniature Image Sensor

The NanoVision demo kit for the NanEyeC is based on an Arduino development platform, and includes all necessary drivers to interface the sensor's Single-Ended Interface Mode output to an Arm Cortex-M7 microcontroller.

[Read more](#)

WEBCAST

Dealing with Legacy in Industrial IoT

Sponsored by: **Innodisk, SuperMicro, DFI**

Date: **January 16, 11:00 a.m. ET**

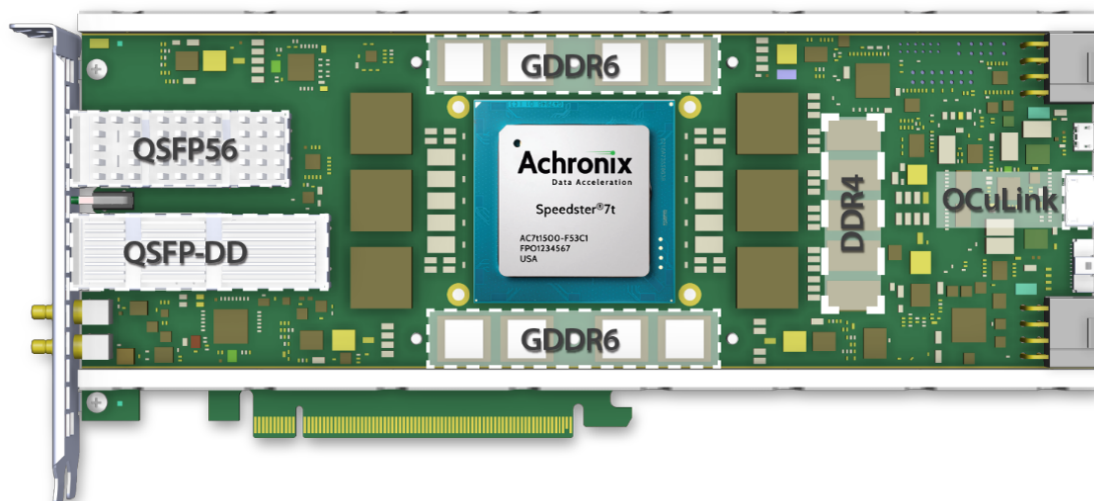
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AI EDGE COMPUTING FEATURE

Achronix Super-Speed FPGA Find a Home on Bittware Board

A short while ago, I wrote about Achronix pushing into the very high-speed FPGA space, with its Speedster7t launch. As you might expect, the next announcement comes from a board vendor who is taking advantage of those ICs.

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