



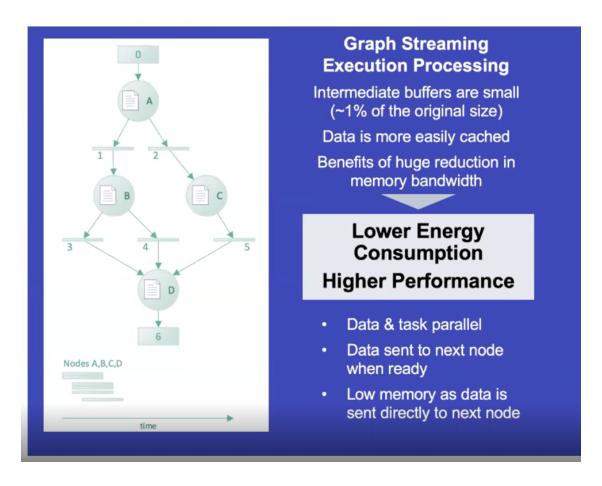
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AI EDGE COMPUTING FEATURE

Neural Network Graphs Need Graph Streaming Processors

As technologies go, neural network processing is still in its infancy. As such, there are still high-level questions that need to be answered. For instance, ? How do you actually execute a neural network graph??

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AUTONOMOUS SENSORS NEWS

VocalZoom Introduces Autonomous Sensors for IIoT

VocalZoom (VZ), a provider of vibration sensors for industry 4.0, launched its Autonomous Sensors for the Industrial Internet of Things (IIoT).

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MACHINE VISION NEWS

Wibu-Systems Serves MVTec Machine Vision Applications

Wibu-Systems has extended the reach of its CodeMeter software protection and licensing platform into MVTec's product portfolio. MVTec's HALCON and

MERLIC software products enable users to build sophisticated machine vision solutions across multiple applications.

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AI SECURITY NEWS

Radiflow and Fraunhofer Launch Joint Research to Apply AI in Industrial Cybersecurity

Radiflow, a provider of cybersecurity solutions for industrial automation networks, and the Fraunhofer Institute of Optronics, System Technologies, and Image Exploitation (IOSB) launched a joint research project for applying advanced machine learning and artificial intelligence to cybersecurity for industrial automation networks.

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SPONSORED WHITE PAPER

How to do Machine Learning on Arm Cortex-M Microcontrollers

Machine learning (ML) algorithms are moving processing to the IoT device due to challenges with latency, power consumption, cost, network, bandwidth, reliability, security, and more.

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WEBCAST

New Innovative TM-PIM Solving Reliability and Robustness Issues in Industrial Drives

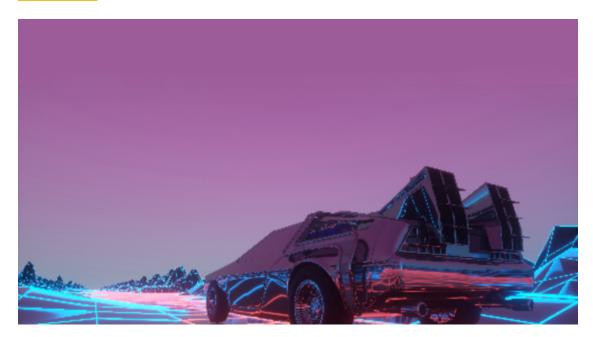
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AI EDGE COMPUTING FEATURE

2020 Embedded Processor Report: Back to the Future with Analog Computing

To illustrate the most basic advantages of analog computing, consider processing analog signals that are described by a set of differential equations. Because continuous time doesn?t exist in a digital computing paradigm, a digital computer must sample the input every clock cycle to develop a sample signal. This can result in many, many computations, which has the cascading effect of higher latency, increased power consumption, and so on.

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