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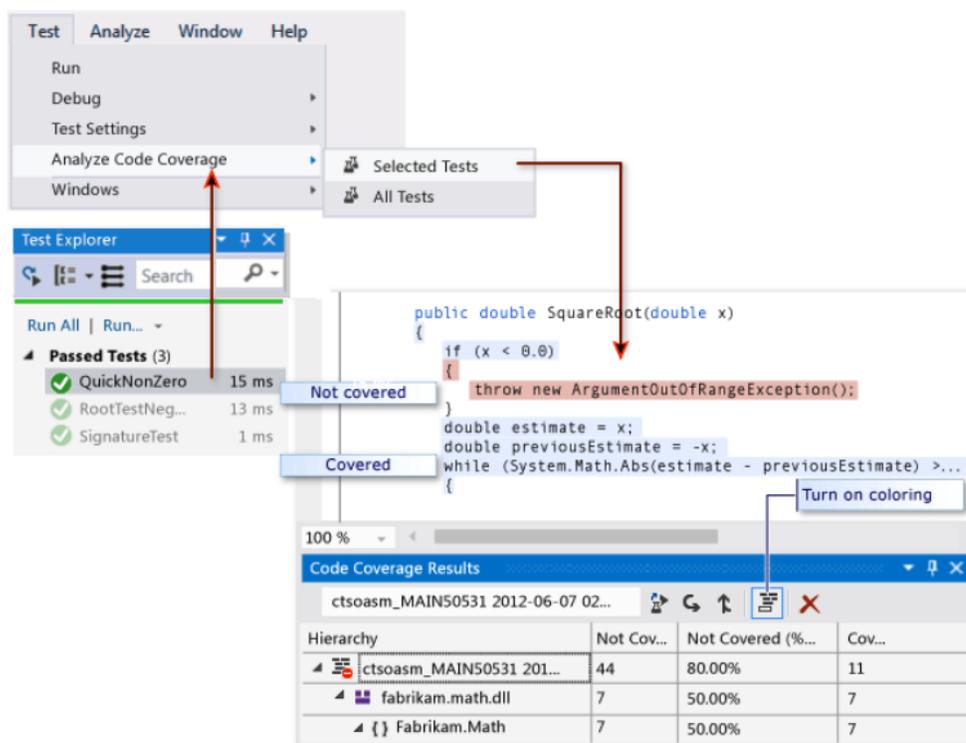


## DEVELOPMENT TOOLS & OPERATING SYSTEMS FEATURE

### Importance of Code Coverage Tools

Test coverage is an important indicator of software quality and an essential part of software maintenance. The real challenge is choosing which code coverage tools to use for application testing. There are several options to create code coverage reports depending on the language(s) used for coding.

[Read more](#)



## DEVELOPMENT TOOLS & OPERATING SYSTEMS NEWS

### Imagination Technologies Releases IMG Edge Services Platform

IMG Edge features state-of-the-art design, verification, and validation solutions supported by Imagination's end-to-end expertise in delivering everything from the lowest-level hardware components to complex consumer devices.

[Read more](#)

Powered by the Intel? Movidius? X VPU and supports OpenVINO?, a toolkit that accelerates solution development and streamlines deployment.

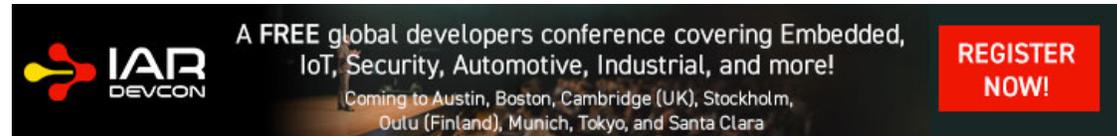
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#### IoT NETWORKING NEWS

### Actility and Hiber Provide Remote IoT Connectivity

An interface links Hiberband with Actility's LoRaWAN connectivity and management solutions, enabling data collected by IoT sensors to be condensed into LoRaWAN packets and distributed throughout the Hiberband network.

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#### POWER ELECTRONICS NEWS

### COSEL's High-Reliability 80W DC-DC Converter Targets Demanding Apps

The MGF80 series' synchronous rectification and soft-switching, among other features, enables a 94 percent efficiency. All models deliver up to 81.6W, but to maintain high performance, the output power of the 3.3V MGFS80243R3 is limited to 59.4W.

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#### AUTOMOTIVE IoT NEWS

### Excelfore to Deploy the eSync In-Vehicle OTA Platform

Providing a secure, bidirectional OTA data pipeline, eSync OTA technology enables Chinese automaker FAW to manage software updates to more than 30 electronic control units and domain controllers from 24 different vendors.

[Read more](#)

#### WEBCAST

### Raspberry Pi 4 and Zulu Embedded Java: The Perfect Combination

Sponsored by: **ON Semiconductor**

Date: **September 24, 2:00 p.m. ET**

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#### IoT NETWORKING FEATURE

### Five Minutes With ... Yunsup Lee, CTO, SiFive

As one of the original authors of the RISC-V instruction set architectures (ISA), Yunsup Lee was part of the RISC-V movement from the ground floor. Currently, Lee is the CTO of SiFive, one of the leaders in terms of RISC-V IP. But this Five Minutes With?discussion revolves around the Chips Alliance, where Lee is a Board Member. The Alliance has a goal of developing open-source hardware, which dovetails perfectly with the motivation behind RISC-V.

[Tune in for more](#)



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