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EDGE COMPUTING FEATURE

IoT Intelligence Moves Toward the Edge

Thanks to developments in neural networks and AI, a new era of distributed intelligence (AI at the edge and in the cloud) is emerging from the centralized IoT intelligence architectures of the past.

[Read more](#)



DEVELOPMENT KIT NEWS

Flex Logix Releases Evaluation Boards for EFLX 4K eFPGA

The validation chip has 4 EFLX cores with manufactured RAM and can operate at GHz speeds. Users can now access EFLX evaluation boards to verify the functionality of their RTL using on-chip PLLs and RAM.

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PCIe-Based FPGA Acceleration Card for Data Centers.

Intel? Programmable Acceleration Card with Arria? 10 GX FPGAs

offers inline and lookaside acceleration and features the Acceleration Stack

for Intel? Xeon? CPU with FPGAs.

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POWER ELECTRONICS NEWS

TI Claims Lowest Quiescent Current, Ultra-Small LDO Linear Regulator

The new regulator features low IQ control at light loads even in dropout conditions, with best-in-class transient response for faster wake-up.

[Read more](#)

POWER ELECTRONICS INDUSTRY NEWS

PSMA Report Addresses Reliability of Power Supply Digital Control Software/Firmware

The report addresses recommended reliability improvements for power supply digital control software and firmware, and presents key findings related to the challenges of and solutions to reliability in the digital control of power electronics.

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EDGE COMPUTING NEWS

congatec Launches 100 Watt Ecosystem For Embedded Edge and Micro Servers

These Server-on-Modules in the COM Express Basic form factor (95 x 125 mm) are available with heat spreaders and heatpipe adapters for even low-profile 1U servers.

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WEBCAST

Raspberry Pi 4 and Zulu Embedded Java: The Perfect Combination

Sponsored by: **Azul Systems**

Date: **September 25, 2:00 p.m. ET**

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EMBEDDED PROCESSING FEATURE

Heterogenous Multicore - Untangling the Technology and Terminology

Predictably, the alternative multicore architecture to homogeneous is heterogeneous, where the various cores have different architectures. This is attractive to the embedded systems developer, as the CPUs may be deployed to the parts of an application for which they are best suited. The software

architecture of such a system is necessarily asymmetric multi-processing (AMP), where each CPU runs its own OS (or maybe no OS at all).

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