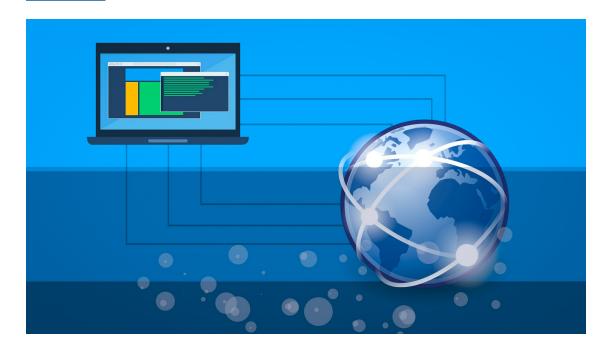


VERIFICATION IP FEATURE

Verification IP: A Vital Component of Chip Design Verification

VIP is considered a valuable component of a verification methodology as it describes libraries of reusable verification components and pre-defined functional blocks instrumental in validating the correctness of complex interfaces and protocols found in system-on-chip (SoC) designs.

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Breker Verification Systems Launches RISC-V TrekApp for Automated, High-Coverage System Integration Test Suite Synthesis

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United Electronic Industries (UEI), providing instrumentation testing, operations and maintenance applications in the aerospace industry, announced the release of its 4th generation stand-alone and embedded controllers.

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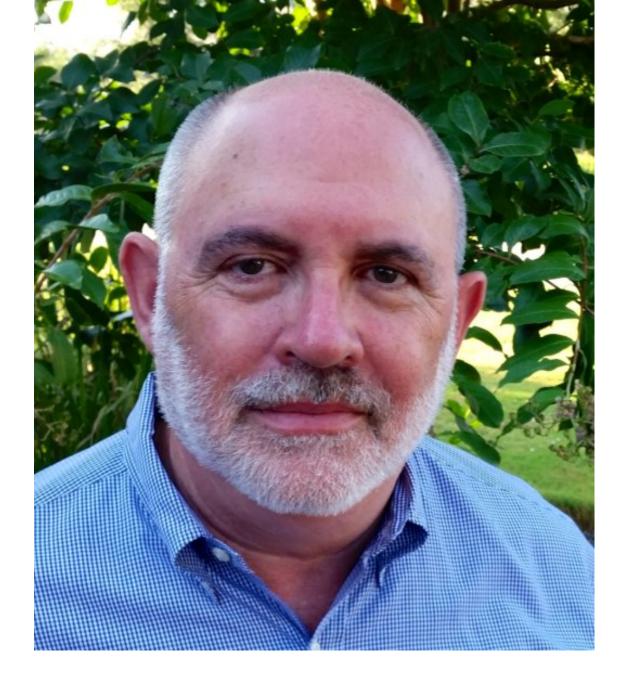
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