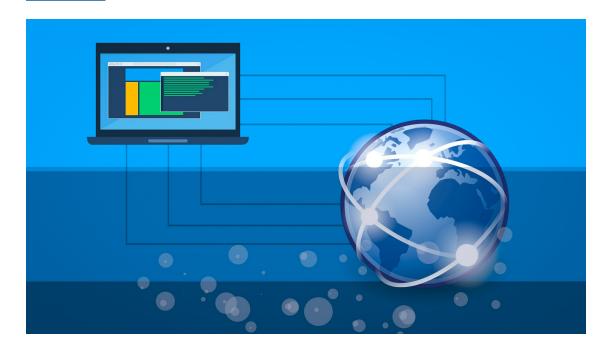**VERIFICATION IP FEATURE**

## Verification IP: A Vital Component of Chip Design Verification

VIP is considered a valuable component of a verification methodology as it describes libraries of reusable verification components and pre-defined functional blocks instrumental in validating the correctness of complex interfaces and protocols found in system-on-chip (SoC) designs.

Read more



**RISC-V NEWS**

**Breker Verification Systems Launches RISC-V TrekApp for Automated, High-Coverage System Integration Test Suite Synthesis**

Breker Verification Systems, a provider of Test Suite Synthesis tools based on the Portable Stimulus Standard (PSS), introduced its RISC-V TrekApp.

Read more

**DUAL CORE ARM PROCESSORS NEWS**

## Programmable Automation Controllers Feature Dual Core ARM processors and new HMI features

United Electronic Industries (UEI), providing instrumentation testing, operations and maintenance applications in the aerospace industry, announced the release of its 4th generation stand-alone and embedded controllers.

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**AUTONOMOUS VEHICLES NEWS**

## CLASS Software Framework Makes Modena Smarter in Real-Life Setting

Software technology developed by the European project CLASS is now being tested on connected cars in the Modena Automotive Smart Area (MASA), an urban laboratory in northern Italy.

Read more

WEBCAST

## Empowering Next-Generation Automotive Wireless Systems Using Advanced Test Solutions

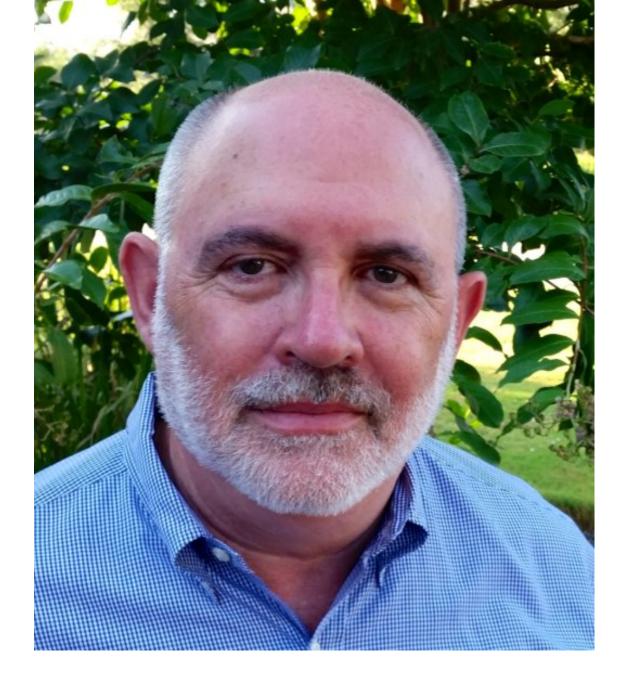Sponsored by: **Anritsu**
Date: **December 12, 2:00 p.m. ET**
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**EMBEDDED EXECUTIVE PODCAST**

## Embedded Executives: Thomas Lee, VP, InnoPhase

The definition of "low power" can differ based on your application. Low power for an industrial system supplier can differ greatly from that of a medical device maker.

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